

## CLAIMS

What is claimed:

1. A flat panel monitor frame, comprising:  
a plurality of frame sections suitable for bordering a flat panel monitor, the frame sections comprising:  
a frame top;  
5 a frame right side extending downward from the frame top; and  
a frame left side extending downward from the frame top;  
at least one planar speaker residing in at least one of the frame sections; and  
a conductor for electrically coupling the at least one planar speaker to a source of at least one speaker signal associated with the flat panel monitor.
2. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one right planar speaker mounted in the frame right side and at least one left planar speaker mounted in the frame left side.
3. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one ribbon speaker.
4. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one quasi-ribbon speaker.
5. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one electrostatic loudspeaker (ESL).
6. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one distributed mode loudspeaker (DML).

7. The flat panel monitor frame of Claim 1, further including a plurality of removable grills disposed over the at least one planar speaker.
8. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker is mounted in an aluminum extrusion.
9. The flat panel monitor frame of Claim 1, wherein a planar speaker processing circuit resides in at least one frame section of the plurality of frame sections.
10. The flat panel monitor frame of Claim 1, further including at least one conventional speaker.
11. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one right planar speaker being mounted in the frame right side and at least one left planar speaker being mounted in the frame left side, and further including at least one right conventional speaker being mounted in the frame right side and at least one left conventional speaker being mounted in the frame left side.
12. The flat panel monitor frame of Claim 1, wherein the at least one planar speaker comprises at least one right planar speaker being mounted in the frame right side and at least one left planar speaker being mounted in the frame left side, and at least one planar speaker being mounted in the frame top.
13. The flat panel monitor frame of Claim 1, further including a frame bottom.
14. The flat panel monitor frame of Claim 1, further including at least one electrical circuit for processing the speaker signal for the at least one planar speaker.
15. The flat panel monitor frame of Claim 1, further including a removable replaceable trim.

16. The flat panel monitor frame of Claim 15, wherein the removable replaceable trim comprises wood.
17. A frame system for a flat panel monitor, comprising:  
a frame including a speaker portion and a trim portion, the frame comprising:  
a frame top;  
a frame right side extending downward from the frame top; and  
5 a frame left side extending downward from the frame top;  
a back support board attached to the frame;  
a plurality of planar speakers residing in the speaker portion,  
a plurality of removable and replaceable grills disposed over the speaker portion; and  
a removable and replaceable trim disposed over the trim portion.
18. A speaker and frame system for a flat panel monitor, comprising:  
a frame comprising:  
a frame top;  
a frame bottom;  
5 a frame right side connected between the frame top and the frame bottom; and  
a frame left side connected between the frame top and the frame bottom;  
at least one right planar speaker housed in the frame right side;  
at least one left planar speaker housed in the frame left side; and  
at least one top planar speaker housed in the frame top.
19. The system of Claim 18, further including at least one circuit for processing at least one speaker signal for the at least one right planar speaker, the at least one left planar speaker, and, the at least one top planar speaker.